



DTE IMP QUESTION'S

1. Number system conversion
2. universal gate, derive basic gates using NAND / NOR
3. Binary to gray conversion
4. K-MAP
5. Full adder
6. Memory
7. De-Morgan's law
8. Propagation delay, Power Dissipation, Fan-out, Basic gate
9. BCD adder using IC 7483
10. 16:1 MUX using 4:1 MUX
11. 2's complement subtraction
12. logic gates with their types
13. R-2R DAC (numerical)
14. BCD addition & binary addition
15. D flip flop
16. SAR ADC
17. SOP & POS example & minterm & maxterm
18. 3bit/4bit/mod 10 asynchronous counter
19. SISO
20. BCD to 7 segment decoder
21. boolean eq. using basic gates & simplification
22. Radix
23. T flip flop
24. compare sync. & async. Counter
25. SR flip-flop